

Advertisement for student Internship (01)

Advt. Number: Summer Internship/SSR/CRG/DST-SERB/VEDG/GAP-3341/2024/01

Applications are invited from the eligible candidates for one position of student Internship under Scientific Social Responsibility (SSR) program of the DST-SERB Core Research Grant Scheme.

The details are as follows:

Title of Project: Design and Modeling of a Futuristic High-Efficiency High-Frequency Vacuum RF Amplifier for Space Satellite Communication.

Project Investigator:

Dr. Amitavo Roy Choudhury, Sr. Principal Scientist, Vacuum Electron Devices Group (VEDG), CSIR-Central Electronics Engineering Research Institute, Pilani-333031, Rajasthan.

Duration of Internship: 2 months starting from June/July 01, 2024.

Essential Qualifications: Pre-final/Final year in B.Tech./M.Sc. (ECE/Electronics/Physics) or equivalent

Stipend: Rs.5000 p.m. for two months only (Maximum Rs.10,000/-)

Last date of application: 30.05.2024

How to Apply: Eligible interested candidates meeting above requirements may submit application along with **one page of research proposal** by email to ykv@ceeri.res.in and cc to arc@ceeri.res.in.

The application should include:

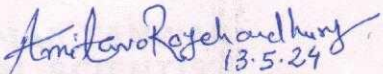
1. A cover letter summarizing the application.
2. One page of research proposal.
3. A recent CV indicating all relevant qualification and contact details.
4. A scanned copy of college photo ID.
5. Recommendation letter from the institute.

The above documents in a single PDF file should be send by email to the mentioned email IDs.

Selection Criteria: Student will be selected based on their academic profile and research proposal. Selected candidate will be intimated by email regarding their selection.

General Condition:

1. Selected student will be supported by a financial assistantship of Rs.10,000/- (@ Rs.5000/- p.m. for two months only).
2. Financial support for other activities like travel, boarding and lodging will not be provided.


13.5.24
Dr. Amitavo Roy Choudhury
PI, GAP-3341
CSIR-CEERI, Pilani