

इंडियन इंस्टीट्यूट ऑफ टेक्नोलॉजी दिल्ली
हौज खास, नई दिल्ली -110016
(औद्योगिक अनुसंधान एवं विकास इकाई)
INDIAN INSTITUTE OF TECHNOLOGY DELHI
Hauz Khas, New Delhi-110016
(Industrial Research & Development Unit)

No. IITD/IRD/RP4714G/ 253576

Dated:11/03/2024

Advertisement No.: IITD/IRD/070/2024

Applications from Indian nationals are invited for Project Appointment under the following project. Appointment shall be on contractual basis with consolidated pay, renewable yearly or upto the duration of the project, whichever is earlier. निम्नलिखित परियोजना के तहत भारतीय नागरिकों में आवेदन आमंत्रित किए जाते हैं। अपॉइंटमेंट, अनुबंधित आधार पर सम्मिलित वेतन, नवीकरणीय वार्षिक या परियोजना की अवधि तक, जो भी पहले हो, के साथ होगा।

Title of the Project	Ultra-compact nano-antennas based on surface acoustic wave actuated magnetolectric nanomagnets. (RP04714G)	
Funding Agency	Science and Engineering Research Board (SERB)	
Name of the Project Investigator	Prof. Dhiman Mallick [email ID: dhiman.mallick@ee.iitd.ac.in]	
Deptt./Centre	Department of Electrical Engineering	
Duration of the Project	Upto:25/02/2027	
Post (s)	Consolidated fellowship / Pay-slab	Qualifications
Jr. Research Fellow (1)	Rs.31,000/- p.m. plus HRA @ 24%	1 st class M.Tech. in Electronics Engineering, Elecgrtrical Engineering, Engineering Physics, Nanotechnology with *NET qualification or 1st class M.Sc. in Physics or Electronics with *NET qualifications or 1st class B.Tech. in Electronics Engineering, Electrical Engineering, Engineering Physics with *GATE or NET qualification. Candidates with prior experiences in semiconductor or sensor device and fabrication will be given preference. *The requirement of NET/GATE examination for the selection to the post of JRF may be relaxed for the candidates who have graduated from Centrally Funded Technical Institutes (CFTIs) with a CGPA of more than 8.000 (80% aggregate marks)

The candidates who are interested to apply for the above post should download **Form No. IRD/REC-4** from the IRD Website (<http://ird.iitd.ac.in/rec>) of IIT Delhi and submit the duly filled form with complete information regarding educational qualifications indicating percentage of marks/division, details of work experience etc. **by e-mail with advertisement No. on the subject line to Prof. Dhiman Mallick at email id: dhiman.mallick@ee.iitd.ac.in**

IIT Delhi reserves the right to fix higher criteria for short-listing of eligible candidates from those satisfying advertised qualification and requirement of the project post and their name will be displayed on web link (<http://ird.iitd.ac.in/shortlisted>) **alongwith the online test and or / interview details. Only short-listed candidates will be informed for online test and of / interview.** In case any clarification is required on eligibility regarding the above post, the candidate may contact to Prof. Dhiman mallick at email id: dhiman.mallick@ee.iitd.ac.in

5% relaxation of marks may be granted to the SC/ST Candidates. In case of selection of a retired/superannuated government employee, his/her salary will be fixed as per prevailing IRD norms. अनुसूचित जाति / अनुसूचित जनजाति के उम्मीदवारों को अंकों की 5% छूट दी जा सकती है। एक सेवानिवृत्त सरकारी कर्मचारी के चयन के मामले में उसका वेतन वर्तमान आईआईटी मानदंडों के अनुसार तय किया जाएगा। **The last date for submitting the completed applications by e-mail is 26/03/2024 by 5.00 p.m.**

वितरण

- Head of the Deptt./Centres/Units : It is requested that the contents of the Above Advt be brought to the notice of the staff working in your Deptt./Centre/Unit
- Webmaster, IRD : To put advertisement at IITD website.
- Notice Boards
- Advertisement file
- Prof. Dhiman Mallick, PI, Department of Electrical Engineering
- Copy to Chairperson, DRC/CRC

महायक कुलमन्त्री, आईआईटी दिल्ली
11/03/2024