



**Indian Institute of Technology (Indian School of Mines), Dhanbad**  
The Office of Dean, Research & Development

<b>Sanction No and Date:</b> 70/0081/23/EMR-II/28/06/2023	<b>IIT (ISM) Project No.</b> CSIR(40)/2023-2024/1045/ECE	<b>Date:</b> 28/11/2023
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**ADDENDUM & CORRIGENDUM**

**JRF position under (COUNCIL OF SCIENTIFIC AND INDUSTRIAL RESEARCH) Project**

Applications are invited under the sponsored project. The details of the project are as under:

<b>Position</b>	Junior Research Fellow
<b>Number of Position (s)</b>	01 (One)
<b>Title of The Project</b>	Memristor Based Multilayer Neural Networks (MNN) And Its Application in Neuromorphic System
<b>Principal Investigator(s)</b>	<b>Dr. Rajeev Kumar Ranjan   PI</b> Department of Electronics Engineering Indian Institute of Technology (ISM) Dhanbad, Dhanbad--826004 Jharkhand, INDIA E-mail: <a href="mailto:rajeev@iitism.ac.in">rajeev@iitism.ac.in</a> Phone No.: +91-326-2235658 (Office), +91-9471191517 (Mobile)
<b>Tenure of Project</b>	36 Months
<b>Job Description (in maximum of 100 words)</b>	The project aims to design and fabricate an analog/digital Multilayer neural Network VLSI chip.
<b>Essential Qualification</b>	B.E/B.Tech or M.E/M.Tech (With at least 55% marks) in Electronics & Communication Engineering or allied field. The candidate should be either qualified in GATE or CSIR/UGC NET. OR M.Sc (With at least 55% marks) in Electronics/Physics and qualified in GATE or CSIR/UGC NET.
<b>Desirable Qualification</b>	The candidate should have basic knowledge of Analog/Digital VLSI along with Embedded systems. Preferably, good skills in programming languages such as C++, Python, Microcontroller/Microprocessor, Verilog/VHDL along with exposure to software such as Cadence, Xilinx, etc. would be an additional advantage.
<b>Age and Relaxation (if any)</b>	The upper age limit is 28 years at the time of appointment (Age relaxation for SC/ST/OBC/PH/Women candidates as per CSIR rules. For more details follow the <a href="https://csirhrdg.res.in/Home/Index/1/Default/3108/63">https://csirhrdg.res.in/Home/Index/1/Default/3108/63</a> )
<b>Fellowship</b>	Rs. 37,000/- for 1 <sup>st</sup> & 2 <sup>nd</sup> Year and Rs. 42,000/- for 3 <sup>rd</sup> Year Plus HRA as per CSIR Norms. HRA will be provided if candidate stays outside the institute.
<b>Last Date &amp; Time</b>	Interested candidates are requested to send their application along with detailed CV and copies of educational certificates, age proof, NET/GATE certificate, experience certificates, category certificate (if applicable) in a single pdf file on or before 20 <sup>th</sup> January 2024 to PI through e-mail ( <a href="mailto:rajeev@iitism.ac.in">rajeev@iitism.ac.in</a> ).

Shortlisted candidates will be informed date of interview by e-mail. Mere possession of minimum qualification does not guarantee an invitation to the interview. Candidates will be short listed based on their merit and as per the requirement of the project. All candidates should make their own arrangements for their stay at Dhanbad, if required. No TA/DA will be paid to attend the interview.

*Rkranjan*  
(Signature of PI)